

Electronic Acknowledgement Receipt

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Application Number:	10072145
International Application Number:	
Confirmation Number:	1448
Title of Invention:	Semiconductor device and method of providing regions of low substrate capacitance
First Named Inventor/Applicant Name:	Guy E. Averett
Correspondence Address:	ON Semiconductor Patent Administration Dept - MD A700 P.O. Box 62890 - Phoenix AZ 85082-2890 US 6022445603 -
Filer:	Kevin Brian Jackson
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Amendment After Final	ONS00317KamekonAmendmentRCE6AmendFnI.pdf	90677 d40480628df5df6549d7c32b33607e32cb73b16f	no	7

Warnings:

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Total Files Size (in bytes):	90677
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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